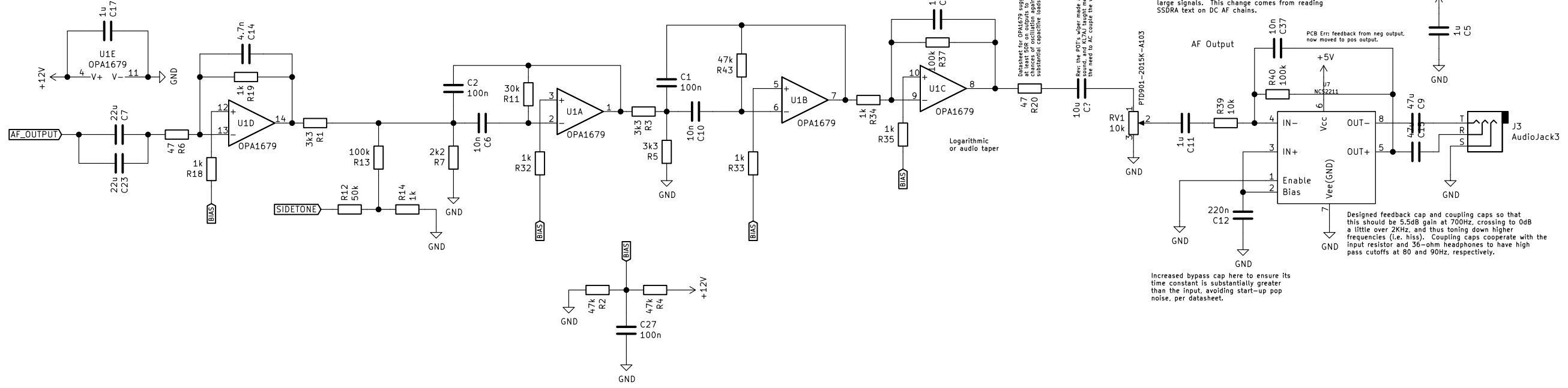


Breadboarded opamp stages, confirming band pass shape peaking at 700Hz, and a total of about 66dB of gain.

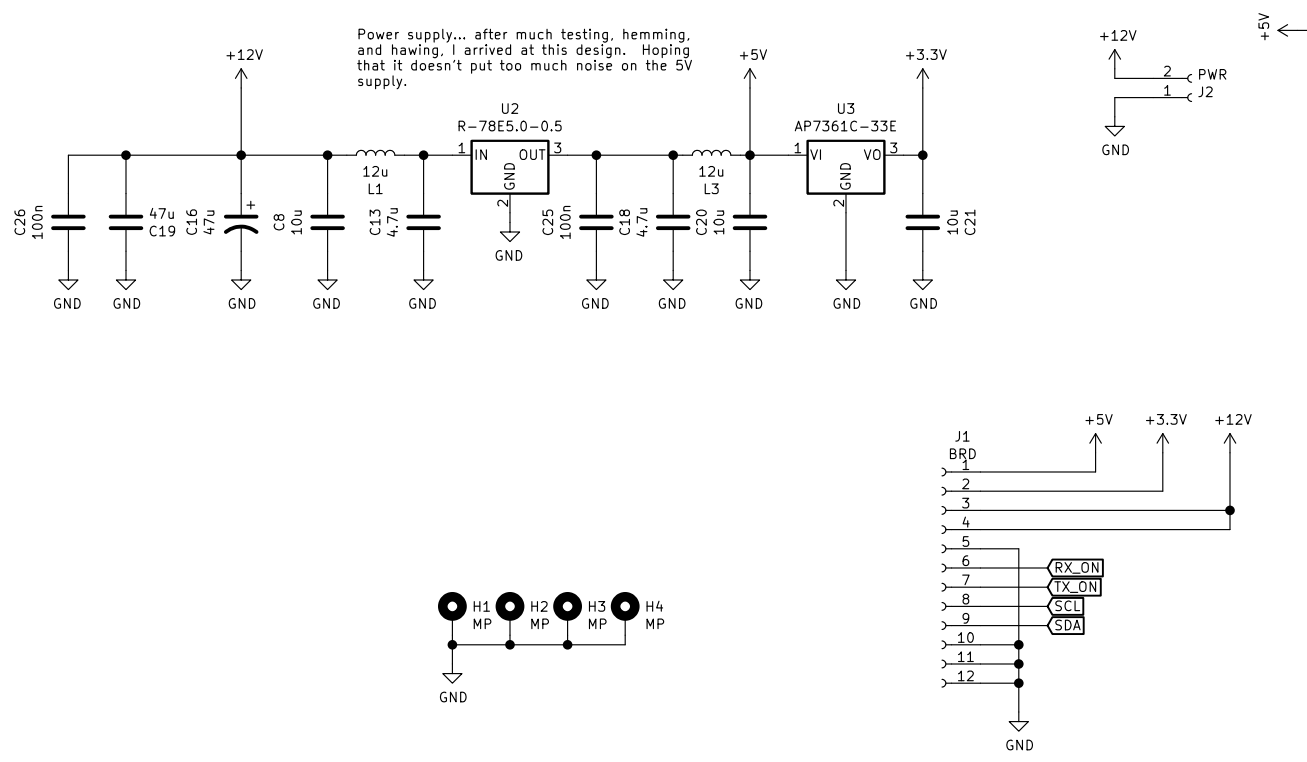


Rev: changed gain distribution, with R40 becoming a 100k resistor, and R19 dropping to a 1k. This improves use of the available 12V rails in the two opamp gain stages, so that I won't get clipping on large signals. This change comes from reading SSDRA text on DC AF chains.

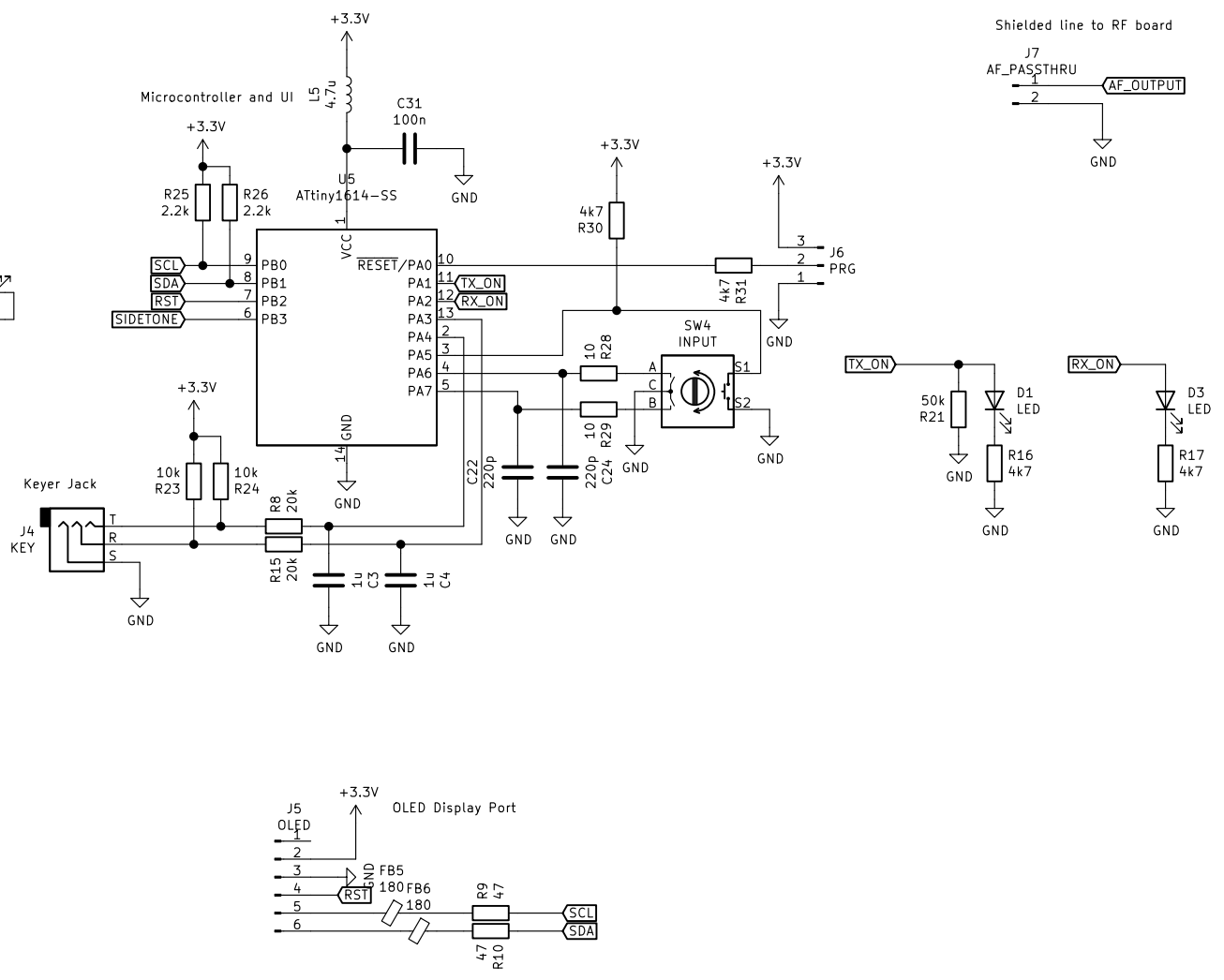
PCB Err: feedback from neg output. Now moved to pos output.

Designed feedback cap and coupling caps so that this should be 5.5dB gain at 700Hz, crossing to 0dB a little over 2KHz, and thus toning down higher frequencies (i.e. hiss). Coupling caps cooperate with the input resistor and 36-ohm headphones to have high pass cutoffs at 80 and 90Hz, respectively.

Increased bypass cap here to ensure its time constant is substantially greater than the input, avoiding start-up pop noise, per datasheet.



Power supply... after much testing, hemming, and hawing, I arrived at this design. Hoping that it doesn't put too much noise on the 5V supply.



Shielded line to RF board

